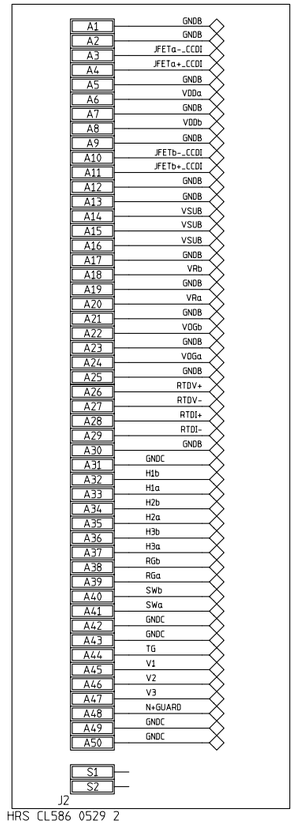
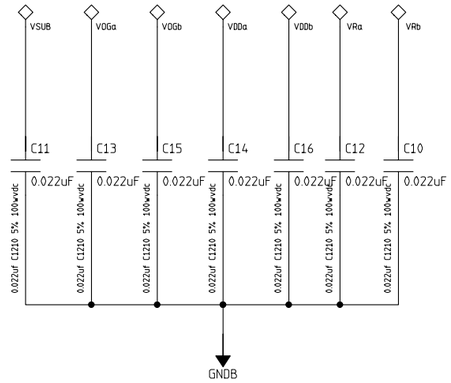
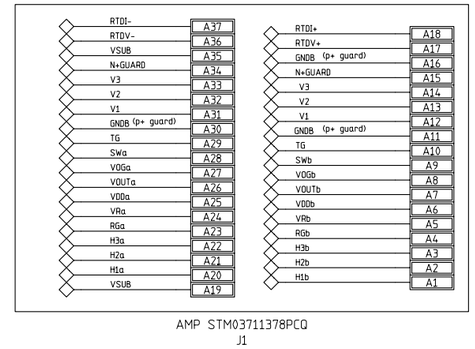
CCD Interface Board with JFET Buffer



FPC Connector

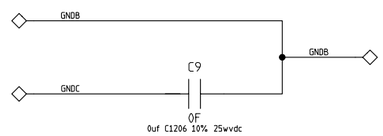




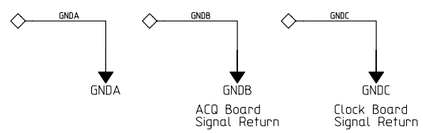
N+guard floats

AMP STM03711378PCQ
J1

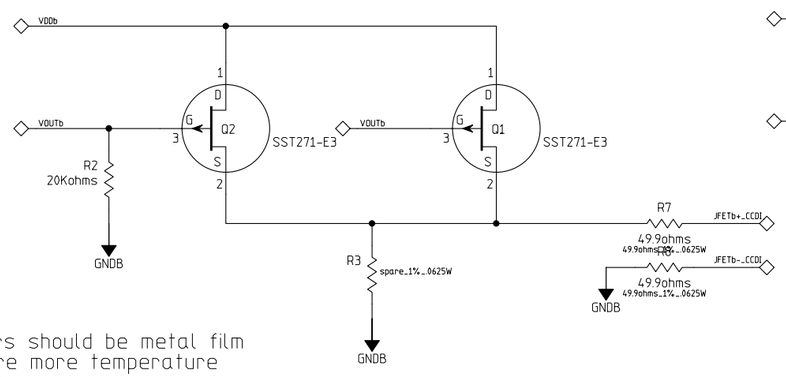
Nanonics Connector

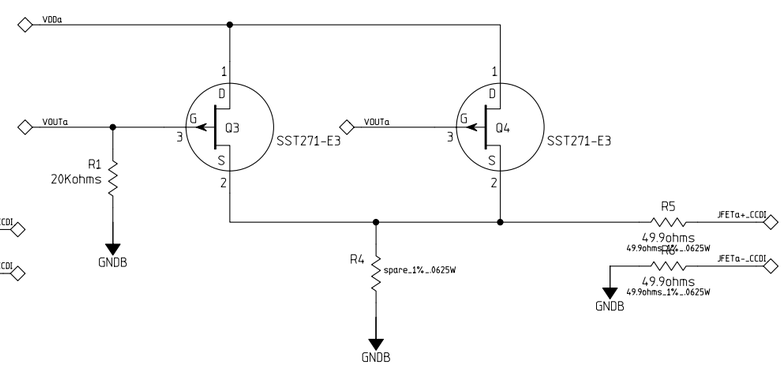


Connection point for returns coming with clocks and biases.



USE GNDB for JFETs?





Resistors should be metal film which are more temperature tolerant.

MODIFIED 10/4/2007 9:36:25 AM			
EED Department	ORIGINATOR	T. Shaw	DATE date
	DRAWN	drawn	DATE date
	CHECKED	checked	DATE date
	APPROVED	approved	DATE date

Fermi National Accelerator Laboratory
United States Department of Energy

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SIZE	DRAWING NUMBER		REV
D	173670	Schematic Page 01 of 01	A

H=8ND; V=4LA