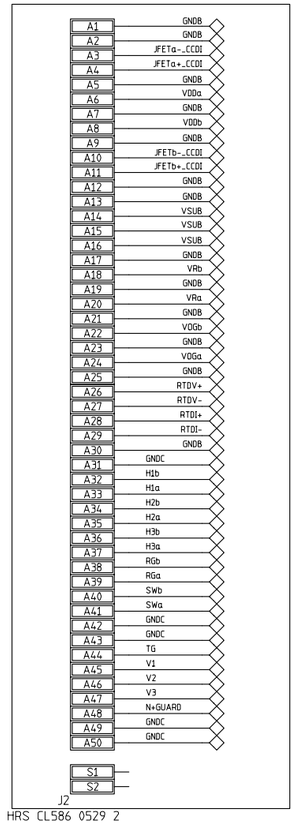
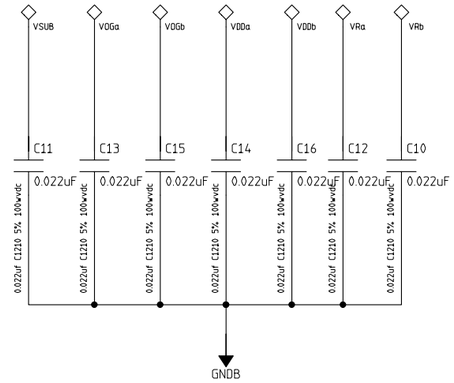
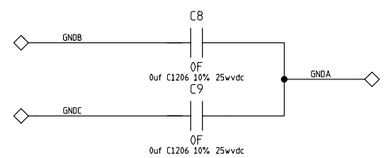
CCD Interface Board with JFET Buffer

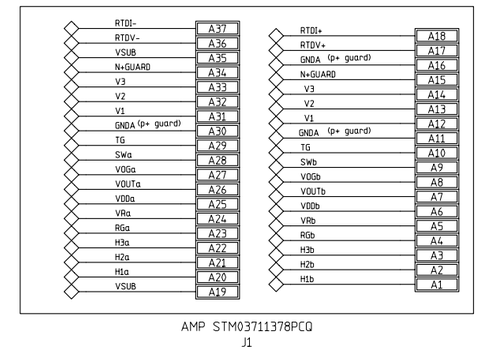


FPC Connector



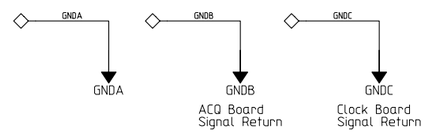


Connection point for returns coming with clocks and biases.

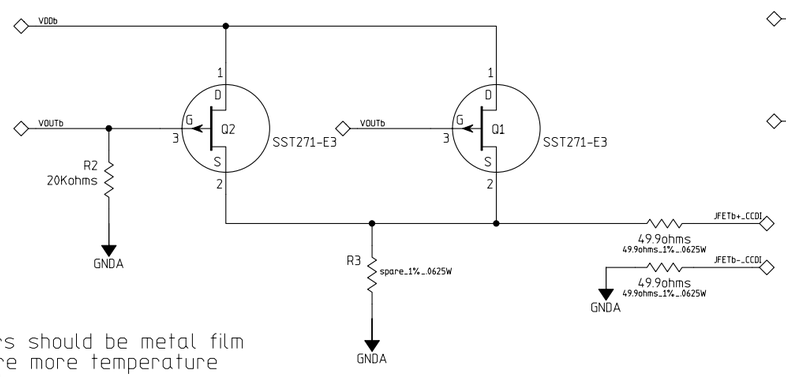


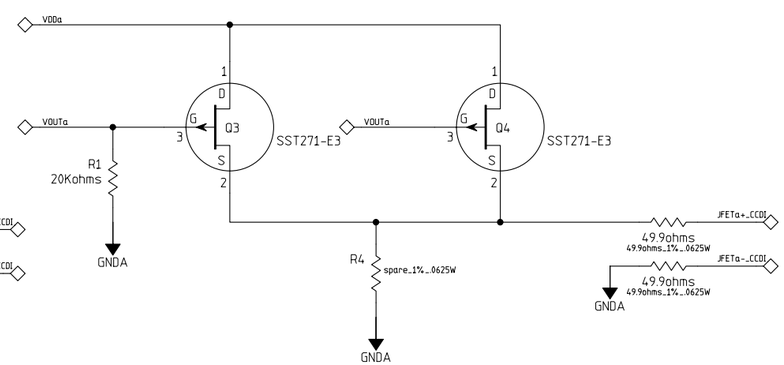
N+guard floats

Nanonics Connector



USE GNDB for JFETs?





Resistors should be metal film which are more temperature tolerant.

MODIFIED 8/24/2007 3:19:01 PM			
EED Department	ORIGINATOR	originator	DATE date
	DRAWN	drawn	DATE date
	CHECKED	checked	DATE date
	APPROVED	approved	DATE date

Fermi National Accelerator Laboratory
United States Department of Energy

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SIZE	DRAWING NUMBER	REV
D	drawing number	A

Schematic Page 01 of 01

H=8ND; V=4LA