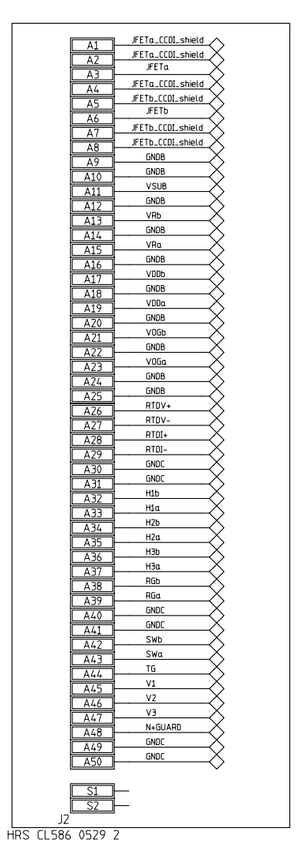
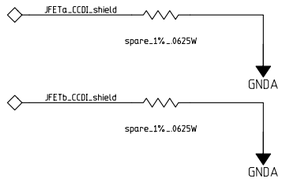
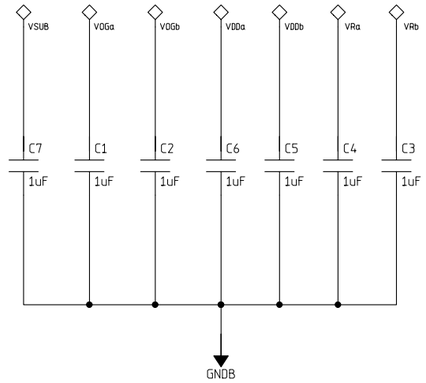
CCD Interface Board with JFET Buffer

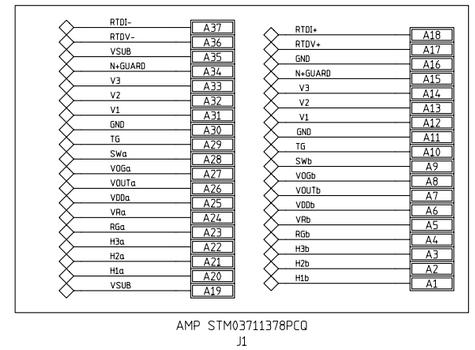


FPC Connector



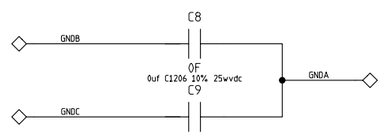


COG Caps -
Can we keep just hi-freq bypass here and leave bulk caps at the other end of the cable? Size is an issue. COG Caps big.
Need to watch wvdc.

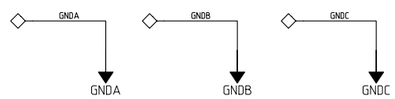


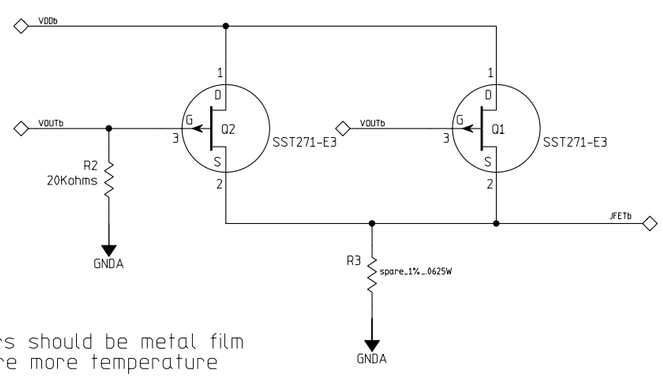
N+guard floats

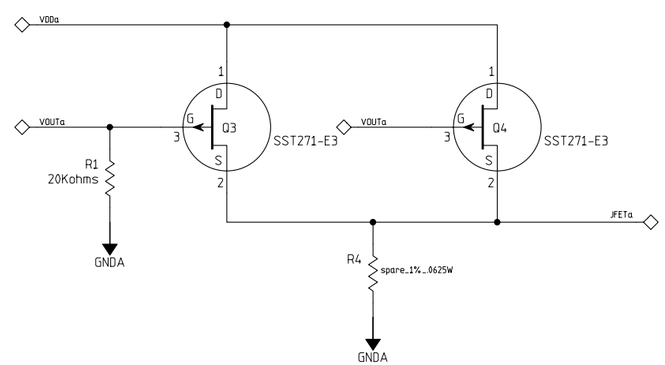
Nanonics Connector



Connection point for refernce coming with clocks and biases.







Resistors should be metal film which are more temperature tolerant.

MODIFIED 8/1/2007 3:53:31 PM			
EED Department	ORIGINATOR	originator	DATE date
	DRAWN	drawn	DATE date
	CHECKED	checked	DATE date
	APPROVED	approved	DATE date

Fermi National Accelerator Laboratory
United States Department of Energy

D:\DES_INTERNAL_CABLE\DES_CCD_Interface_v5\DES_CCD_Interface_v5.prj

SIZE	DRAWING NUMBER	REV
D	drawing number	A

Schematic Page 01 of 01

H=8ND; V=4LA