SEE PAGE 2 FOR REVISION BLOCK

CCD ACQUISITION BOARD
High Voltage Bias and
Digital Output Port Outputs

LOCAL 3.3V REGULATOR FOR TRANSLATORS ONLY

U5
TPS75233Q
RESET, SENSE, OUT, OUT, GND, IN, IN, EN

6.81K R50
C21 47uF 10V
C52 0.1uF
C51 0.1uF
C22 47uF 10V
PSVIN

U3, U2, U4, U1
DUAL SUPPLY TRANSLATING TRANSCEIVER
LVX3245MTC
C23, C24, C19, C20, C25, C26, C16, C18 0.1uF
R49 OPEN

RJ4:A
CPCI A RJ1RJ4 110

RJ4:B
CPCI A RJ1RJ4 110
Z-row is GND
SHIELD
TO PAGE 2

Interface Card
Digital Output Port

P2
AMP 104130-6

Interface Card
High Voltage Bias Outputs

P3
AMP 104130-7

R48, R46, R47, R43, R42, R41, R38, R34, R23, R24, R19, R28, R17, R18, R12, R13
R36, R37, R32, R33, R38, R31, R26, R27, R11, R18, R6, R3, R4, R5, R1, R2

C1, C34, C2, C35, C4, C38, C3, C37, C8, C33, C5, C40, C7, C43, C6, C42, C9, C41, C10, C45, C11, C44, C13, C46, C12, C48, C14, C47, C15, C50, C17, C49 SELECT

CR17, CR19, CR22, CR25, CR26, CR27, CR31, CR32, CR1, CR4, CR5, CR7, CR10, CR14, CR12, CR16, CR18, CR20, CR21, CR24, CR23, CR28, CR29, CR30, CR2, CR3, CR6, CR8, CR9, CR11, CR13, CR15 BAT54S

R9, R8, R28, R7, R14, R13, R29, R15, R22, R30, R35, R21, R25, R46, R49, R44 OPEN

PROTECTION DIODES

D8, D7, D2, D1, D6, D5, D3, D4 SELECT
+PROT_1!, +PROT_2!, -BIASRTN

PROTECTION CLAMPS

Modified Date:Thu Feb 02, 2006
Print Date: Thu Feb 02, 2006

UNLESS OTHERWISE NOTED
Resistors are in Ohms 1/4W +-5%
Capacitors are in micro Farads uF +- 20%
Inductors are in micro Henrys uH
DIG GND MGND AGND EGND

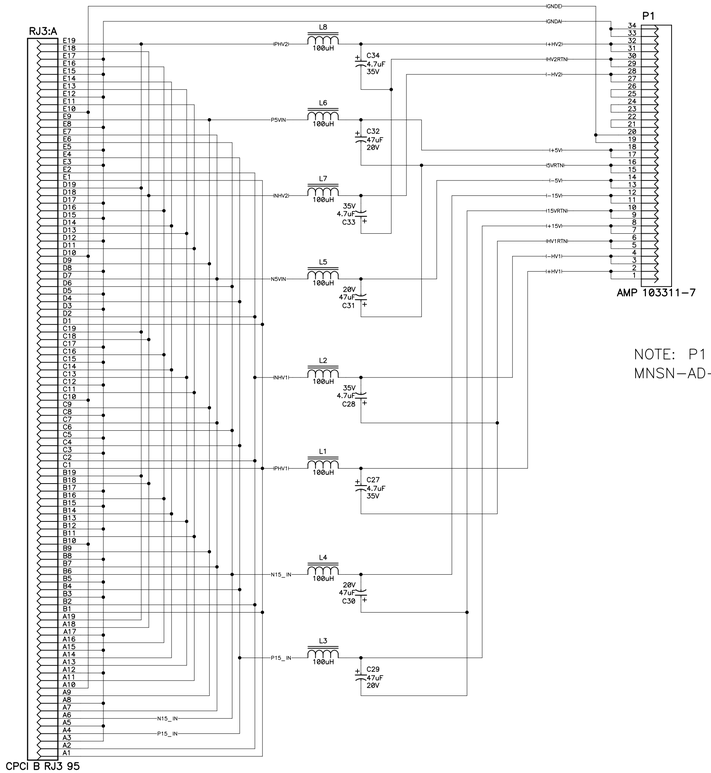
NATIONAL OPTICAL ASTRONOMY OBSERVATORIES
OPERATED BY THE
ASSOCIATION OF UNIVERSITIES FOR RESEARCH IN ASTRONOMY
UNDER COOPERATIVE AGREEMENT WITH
NATIONAL SCIENCE FOUNDATION

DO NOT SCALE DRAWING	NAME: SCHEMATIC CCD Acq Transition Board	USED ON: MONSOON	REF:
NEXT ASSEMBLY: MNSN-EL-04-0009		DWG SIZE: D	REV: -1
SCALE:	DESIGNED BY: Peter Moore	DATE: 21th Sept 2004	CHECKED BY / DATE
DWG PRODUCED USING: PCAD 2004	DRAWN BY: Peter Moore	DATE	APPROVED BY / DATE
	PRINT ISSUED	DATE	APPROVED BY / DATE
DWG NO: MNSN-EL-04-2009	RELEASED	Sheet 1 of 2	

REVISIONS					
LTR	DESCRIPTION	ECR	DATE	BY	APPVD
−1	move rev table to pg 2, add resistor to P4			dms	p.moore

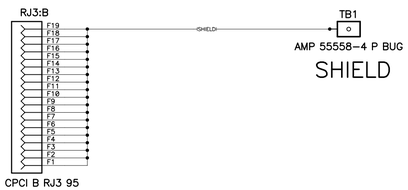
ANALOG POWER

CLK & BIAS BOARD
Power Connector

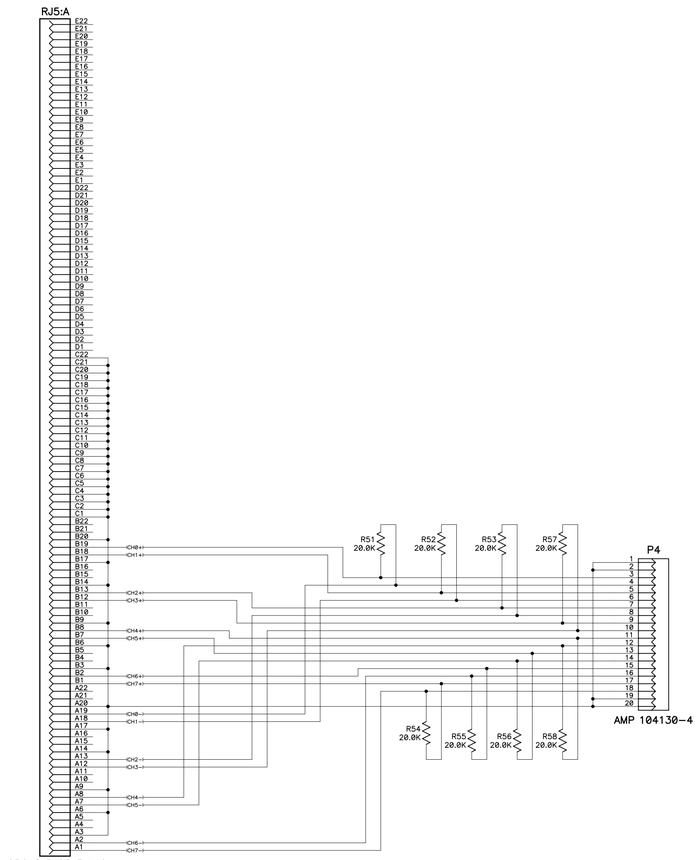


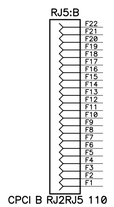
Interface Card
Power Input

NOTE: P1 HAS A POSITIONAL RESTRICTION SEE DRAWING MNSN−AD−01−0010 FOR PCB CARD DIMENSIONS



CCD ACQUISITION BOARD
Video inputs





Z−row is GND

NATIONAL OPTICAL ASTRONOMY OBSERVATORIES			
NAME: SCHEMATIC CCD Acq Transition Board			
DWG NO: MNSN−EL−04−2009	SIZE: D	REF:	REV: −1
RELEASED			Sheet 2 of 2

Modified Date: Thu Feb 02, 2006
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