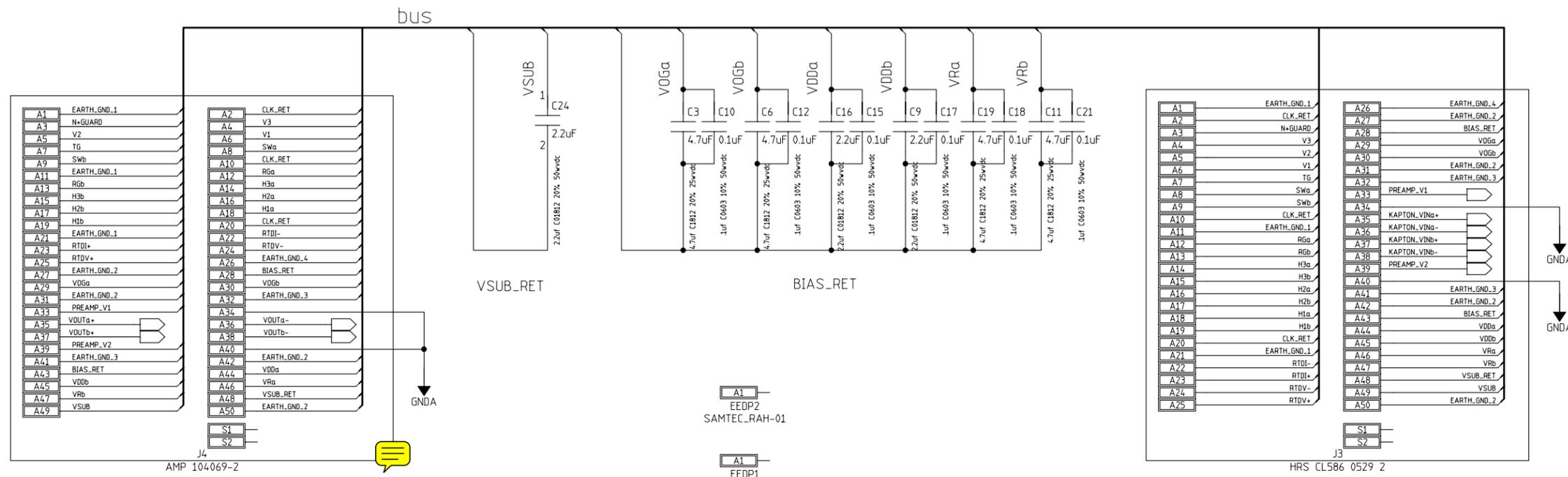


VIB Interface_v2

Microcoax video + preamp



Mezzanine Connector

Clock Group	Bias Group	Video Group	RTD Group
EARTH_GND_1	EARTH_GND_2	EARTH_GND_3	RTDI-
CLK_RET	BIAS_RET	VOUTa+	RTDI+
N+GUARD	VOGa	VOUTa-	RTDV-
SWa	VOGb	VOUTb+	RTDV+
SWb	VDDa	VOUTb-	EARTH_GND_4
RGa	VDDb	PREAMP_GND	
RGb	VRa	PREAMP_V1	
H3a	VRb	PREAMP_V2	
H3b	VSUB		
H2a	VSUB_RET		
H2b			
H1a			
H1b			
TG			
V1			
V2			
V3			

Description:

PC board that acts as a bridge between the Vacuum Circuit Interface Board and the FPC board

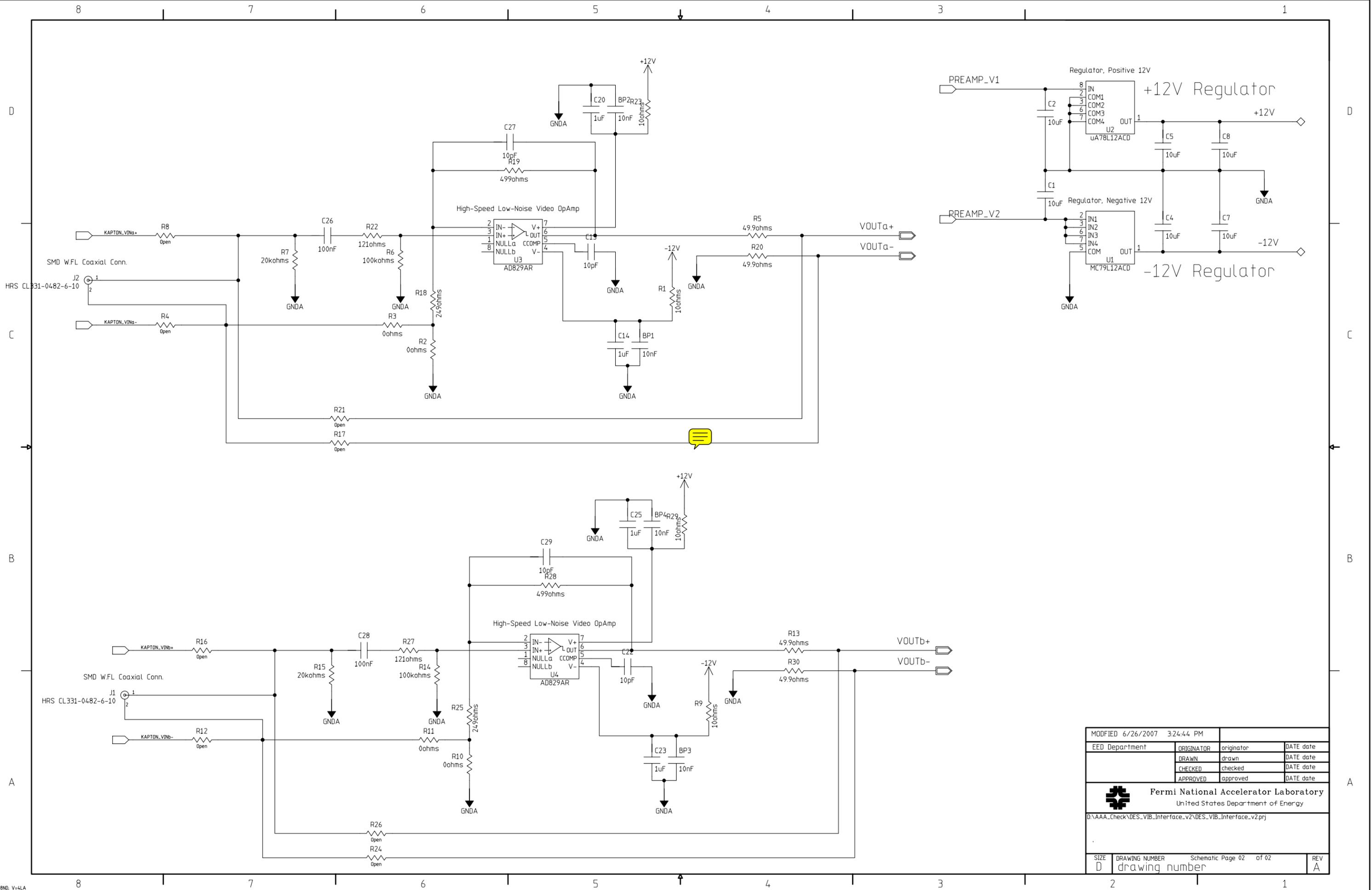
Nominal board outline 2" x 1.3"

Nominal board stackup: Shield, Trace, Trace, Shield, GNDA, Preamp Power

Clock, bias and video groups are each shielded between EARTH_GND_n shapes and shield traces.

Add fuses for PREAMP_V1 and PREAMP_V2 ?

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	DRAWN	drawn	DATE date
	CHECKED	checked	DATE date
	APPROVED	approved	DATE date
 Fermi National Accelerator Laboratory United States Department of Energy			
D:\AAA_Check\DES_VIB_Interface_v2\DES_VIB_Interface_v2.prj			
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