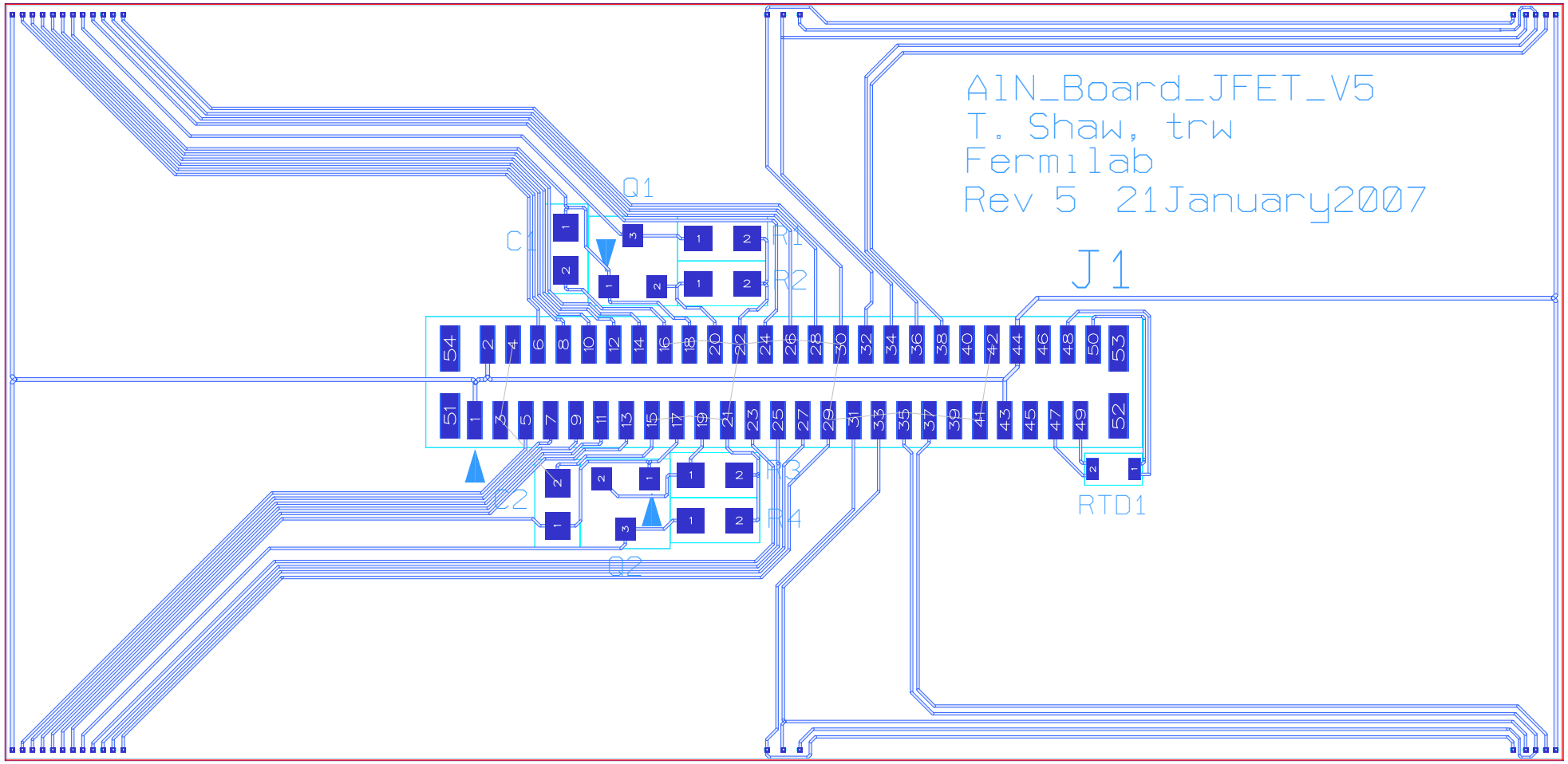
AlN_Board_JFET_V5
T. Shaw, trw
Fermilab
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J1
Q1
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